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**MS AF AMENDMENT
EXPEDITED PROCEDURE UNDER
37 C.F.R. § 1.116
PATENT
1248-0497P**

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HARUHIKO, et al. Conf.: 3009
Appl. No.: 09/552,701 Group: 2113
Filed: April 19, 2000 Examiner: Emerson PUENTE
For: MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF AND IC CARD PACKING
MICROCOMPUTER HAVING BUILT-IN
NONVOLATILE MEMORY AND CHECK SYSTEM
THEREOF

REPLY UNDER 37 C.F.R. § 1.116

MS AF AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 11, 2005

Sir:

In reply to the Office Action dated May 11, 2005, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes a new Claim Set; and Remarks.